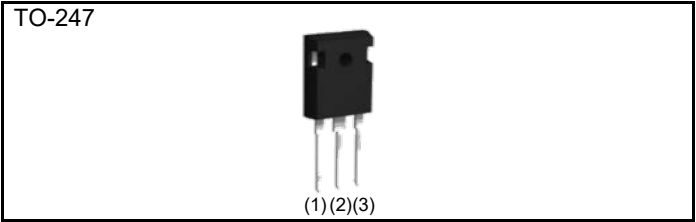
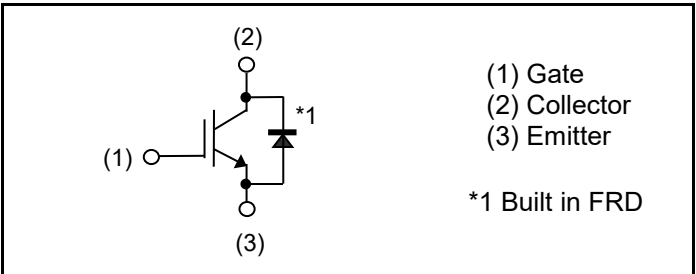


V_{CES}	1200V
$I_C (100^\circ\text{C})$	40A
$V_{CE(sat)} (Typ.)$	2V
P_D	555W

●Outline



●Inner Circuit



●Features

- 1) Low Collector - Emitter Saturation Voltage
- 2) Short Circuit Withstand Time 10 μ s
- 3) Built in Very Fast & Soft Recovery FRD

●Application

General Inverter and Industrial Use

●Absolute Maximum Ratings (at $T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector - Emitter Voltage	V_{CES}	1200	V
Gate - Emitter Voltage	V_{GES}	± 30	V
Collector Current	$T_C = 25^\circ\text{C}$	I_C	80 A
	$T_C = 100^\circ\text{C}$	I_C	40 A
Pulsed Collector Current	I_{CP}^{*1}	160	A
Diode Forward Current	$T_C = 25^\circ\text{C}$	I_F	80 A
	$T_C = 100^\circ\text{C}$	I_F	40 A
Diode Pulsed Forward Current	I_{FP}^{*1}	160	A
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	417 W
	$T_C = 100^\circ\text{C}$	P_D	167 W
Operating Junction Temperature	T_j	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to +155	$^\circ\text{C}$

*1 Pulse width limited by T_{jmax} .

●Thermal Resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.3	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	0.6	°C/W

●IGBT Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Collector - Emitter Breakdown Voltage	BV_{CES}	$I_C = 1\text{mA}, V_{GE} = 0\text{V}$	1200	-	-	V
Collector Cut - off Current	I_{CES}	$V_{CE} = 1200\text{V}, V_{GE} = 0\text{V}$	-	-	10	μA
		$T_j = 150^\circ\text{C}$	-	-	1	mA
Gate - Emitter Leakage Current	I_{GES}	$V_{GE} = \pm 30\text{V}, V_{CE} = 0\text{V}$	-	-	± 500	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5\text{V}, I_C = 1\text{mA}$	4.5	5.5	6.5	V
Collector - Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 40\text{A}, V_{GE} = 15\text{V}$	-	2	-	V
		$T_j = 150^\circ\text{C}$	-	2.7	-	V

●IGBT Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	$V_{CE} = 25\text{V}$	-	2350	-	pF
Output Capacitance	C_{oes}	$V_{GE} = 0\text{V}$	-	195	-	
Reverse transfer Capacitance	C_{res}	$f = 1\text{MHz}$	-	115	-	
Total Gate Charge	Q_g	$V_{CC} = 960\text{V}$	-	180	-	nC
Gate - Emitter Charge	Q_{ge}	$I_C = 40\text{A}$	-	24	-	
Gate - Collector Charge	Q_{gc}	$V_{GE} = 15\text{V}$	-	105	-	
Turn - on Delay Time	$t_{d(on)}$	$I_C = 40\text{A}, V_{CC} = 600\text{V},$ $V_{GE} = 15\text{V}, R_G = 10\Omega,$ $T_j = 25^\circ\text{C}$	-	30	-	ns
Rise Time	t_r		-	78	-	
Turn - off Delay Time	$t_{d(off)}$		-	260	-	
Fall Time	t_f		-	108	-	
Turn-on Switching Loss	E_{on}		-	2.50	-	mJ
Turn-off Switching Loss	E_{off}		-	2.00	-	
Turn - on Delay Time	$t_{d(on)}$	$I_C = 40\text{A}, V_{CC} = 600\text{V},$ $V_{GE} = 15\text{V}, R_G = 10\Omega,$ $T_j = 150^\circ\text{C}$	-	28	-	ns
Rise Time	t_r		-	82	-	
Turn - off Delay Time	$t_{d(off)}$		-	282	-	
Fall Time	t_f		-	140	-	
Turn-on Switching Loss	E_{on}		-	2.80	-	mJ
Turn-off Switching Loss	E_{off}		-	2.20	-	
Reverse Bias Safe Operating Area	RBSOA	$I_C = 120\text{A}, V_{CC} = 1050\text{V}$ $V_p = 1200\text{V}, V_{GE} = 15\text{V}$ $R_G = 50\Omega, T_j = 175^\circ\text{C}$	FULL SQUARE			-
Short Circuit Withstand Time	t_{sc}	$V_{CC} \leq 600\text{V}$ $V_{GE} = 15\text{V}, T_j = 25^\circ\text{C}$	10	-	-	μs
Short Circuit Withstand Time	t_{sc}^{*2}	$V_{CC} \leq 600\text{V}$ $V_{GE} = 15\text{V}, T_j = 150^\circ\text{C}$	8	-	-	μs

*2 Design assurance without measurement

●FRD Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Diode Forward Voltage	V_F	$I_C = 40\text{A}$ $T_j = 25^\circ\text{C}$	-	2.8	-	V
Diode Reverse Recovery Time	t_{rr}		-	130	-	ns
Diode Peak Reverse Recovery Current	I_{rr}	$I_F = 40\text{A}$ $V_R = 400\text{V}$	-	21.7	-	A
Diode Reverse Recovery Charge	Q_{rr}	$di_F/dt = 500\text{A}/\mu\text{s}$ $T_j = 25^\circ\text{C}$	-	1100	-	nC
Diode Reverse Recovery Energy	E_{rr}		-	568	-	μJ
Diode Reverse Recovery Time	t_{rr}		-	337	-	ns
Diode Peak Reverse Recovery Current	I_{rr}	$I_F = 40\text{A}$ $V_R = 400\text{V}$	-	29.1	-	A
Diode Reverse Recovery Charge	Q_{rr}	$di_F/dt = 500\text{A}/\mu\text{s}$ $T_j = 25^\circ\text{C}$	-	5.9	-	μC
Diode Reverse Recovery Energy	E_{rr}		-	2504	-	μJ

●Electrical Characteristic Curves

Fig.1 Power Dissipation vs. Case Temperature

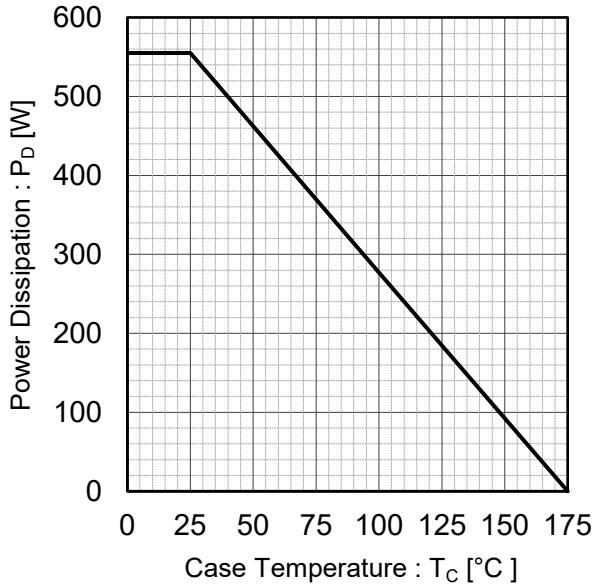


Fig.2 Collector Current vs. Case Temperature

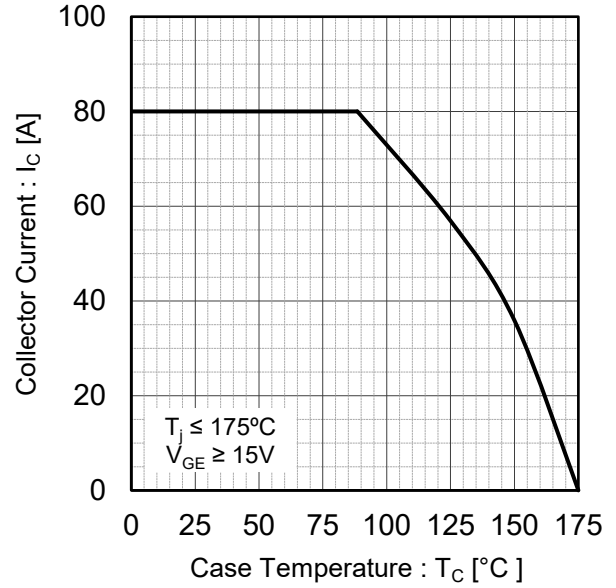


Fig.3 Forward Bias Safe Operating Area

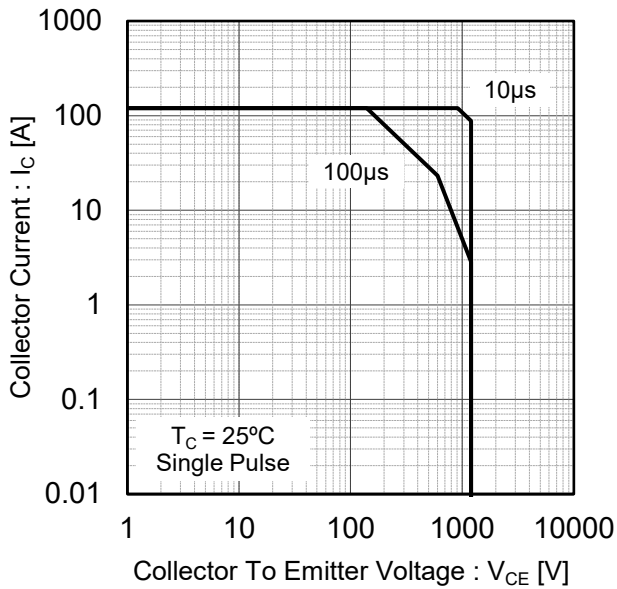
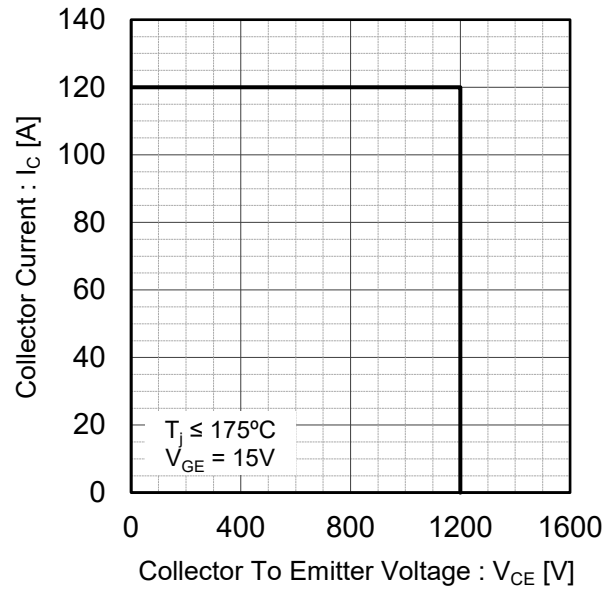


Fig.4 Reverse Bias Safe Operating Area



●Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

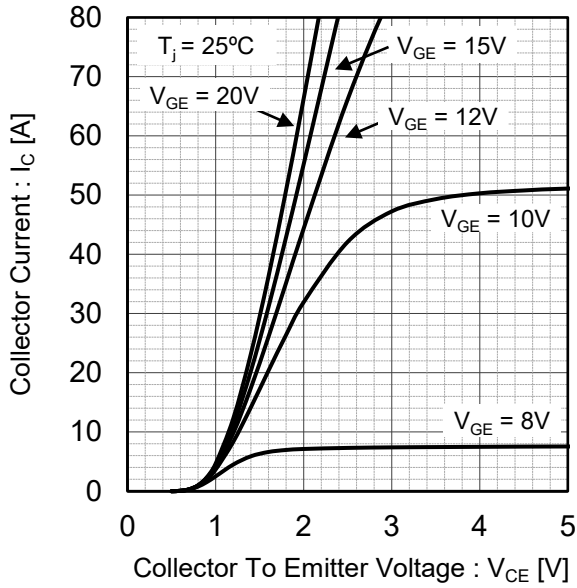


Fig.6 Typical Output Characteristics

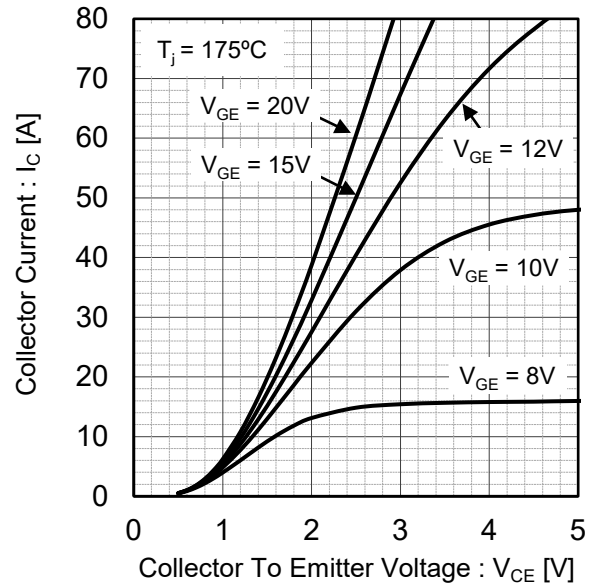


Fig.7 Typical Transfer Characteristics

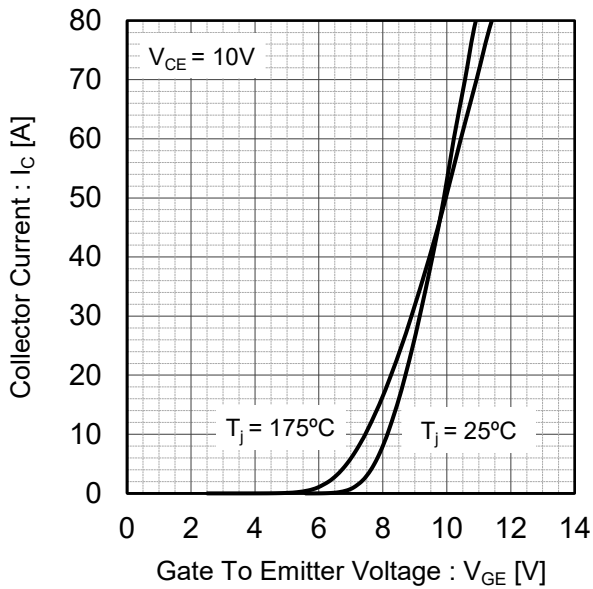
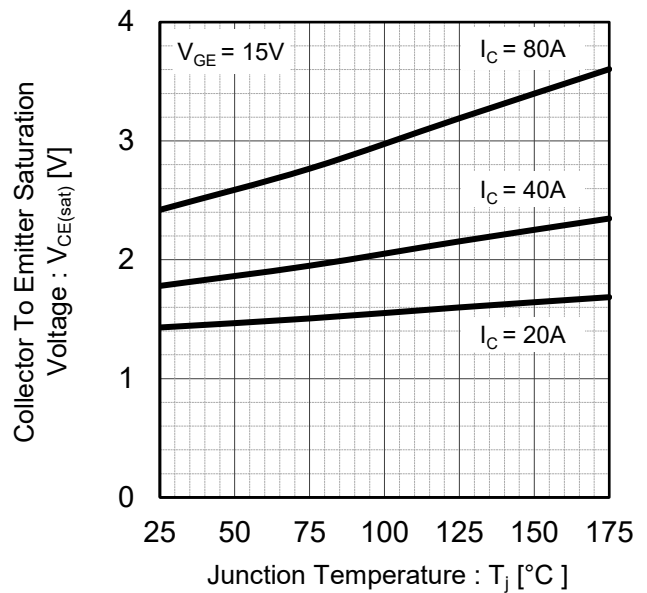


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



●Electrical Characteristic Curves

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

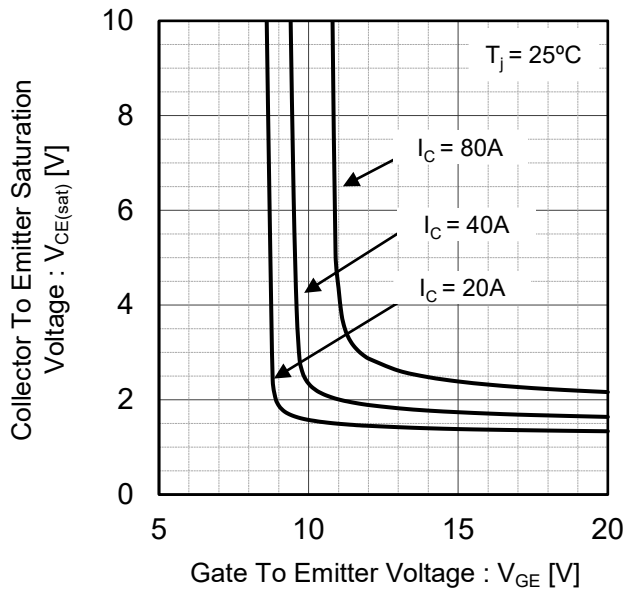


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

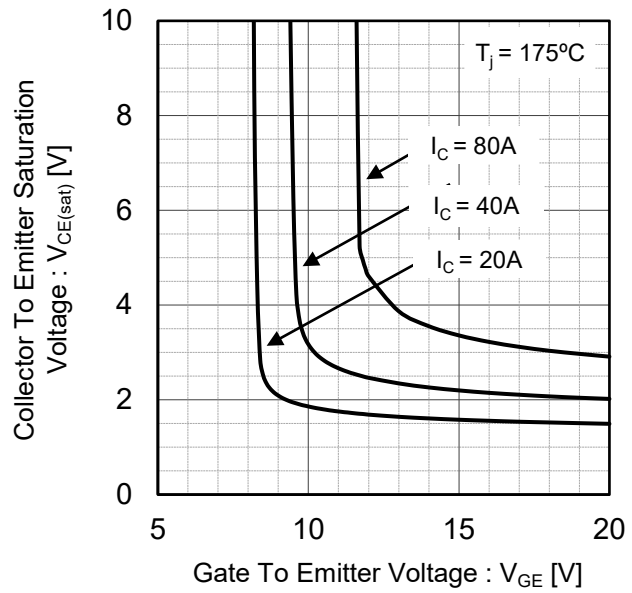


Fig.11 Typical Switching Time vs. Collector Current

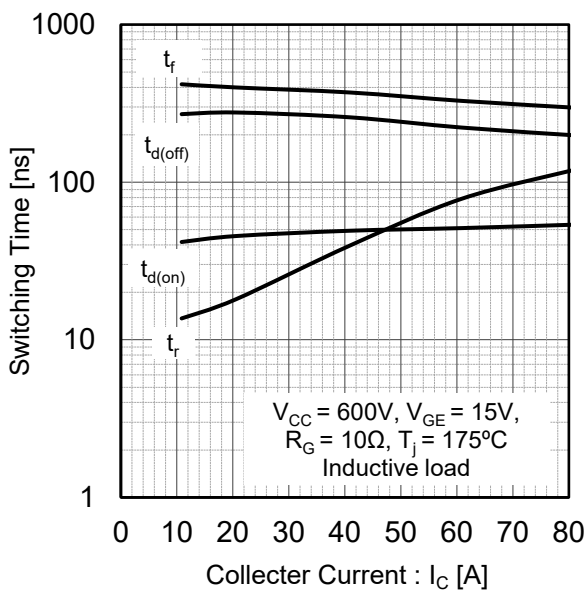
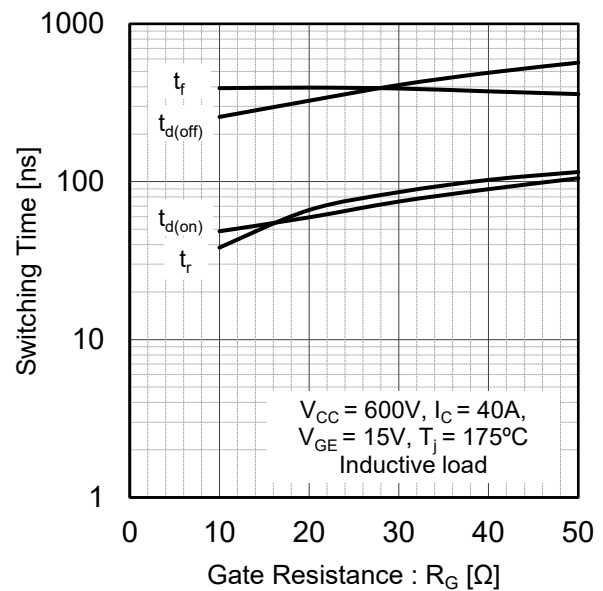


Fig.12 Typical Switching Time vs. Gate Resistance



●Electrical Characteristic Curves

Fig.13 Typical Switching Energy Losses vs. Collector Current

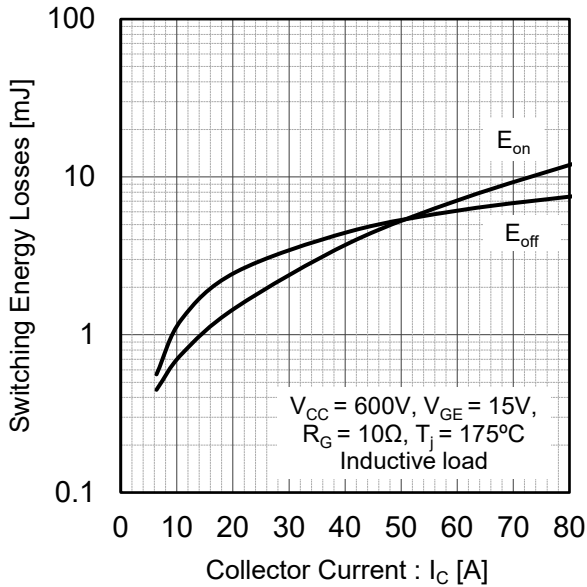


Fig.14 Typical Switching Energy Losses vs. Gate Resistance

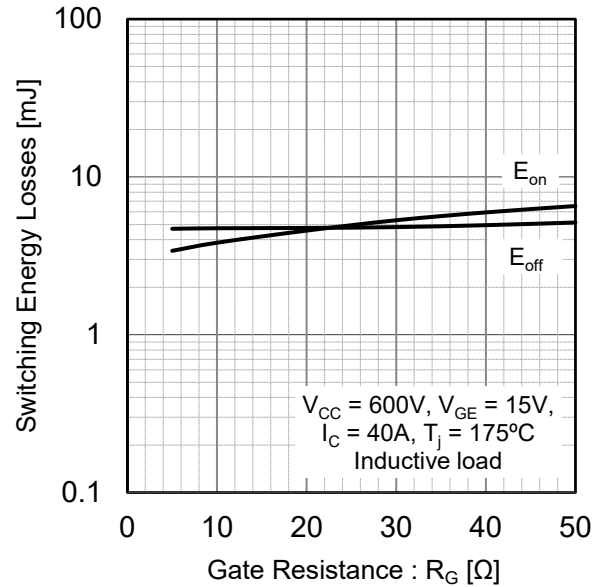


Fig.15 Typical Capacitance vs. Collector To Emitter Voltage

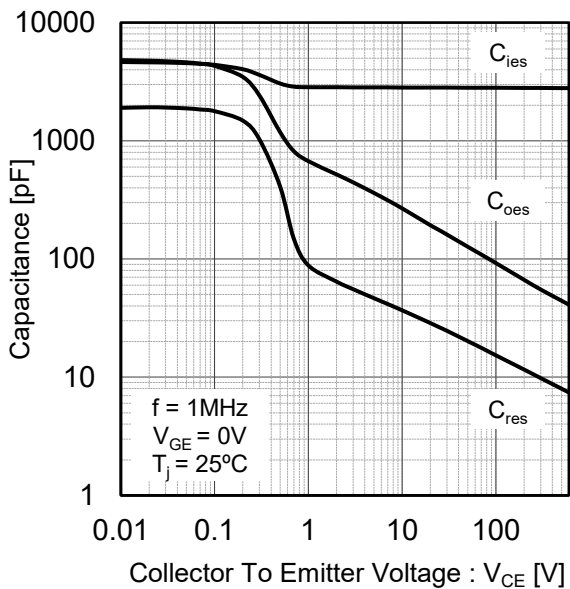
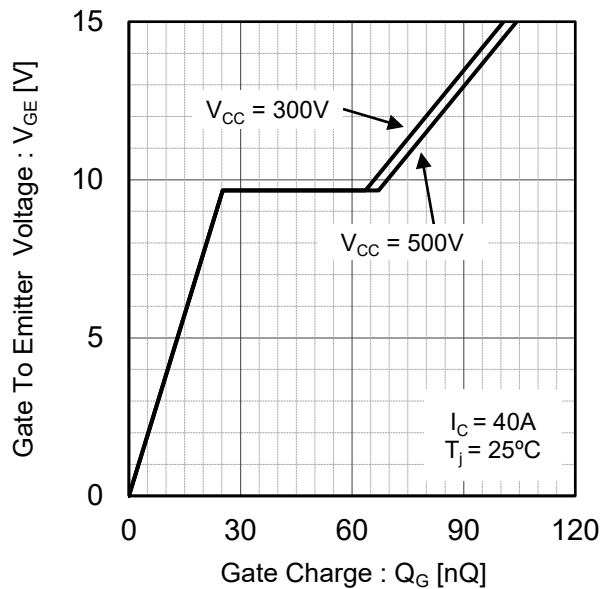


Fig.16 Typical Gate Charge



●Electrical Characteristic Curves

Fig.17 Typical Diode Forward Current vs. Forward Voltage

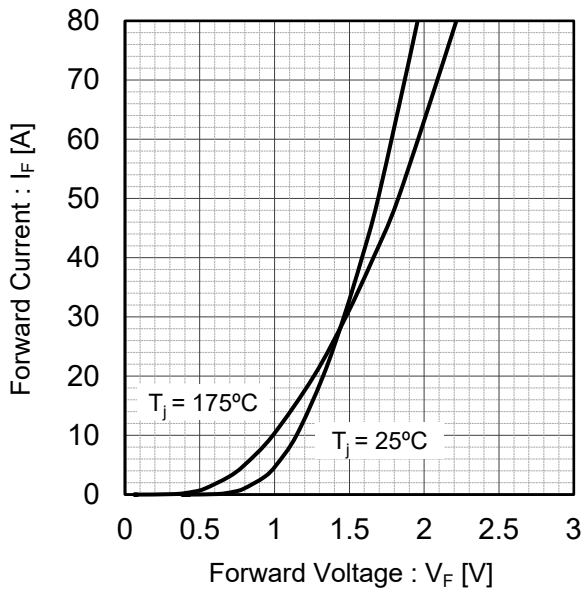


Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current

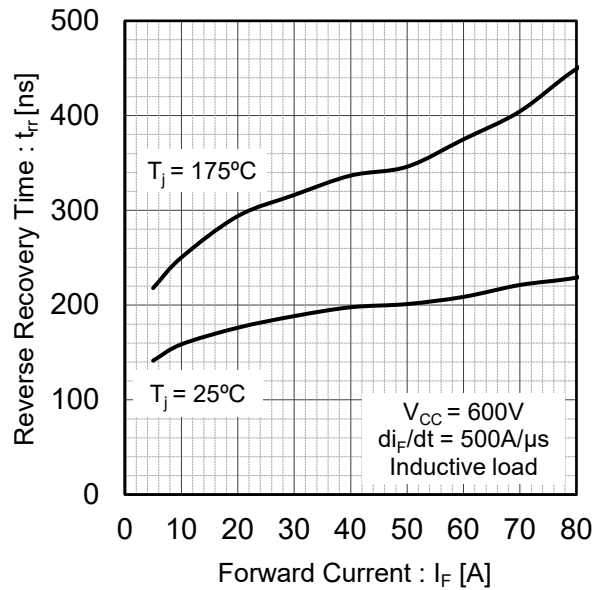


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

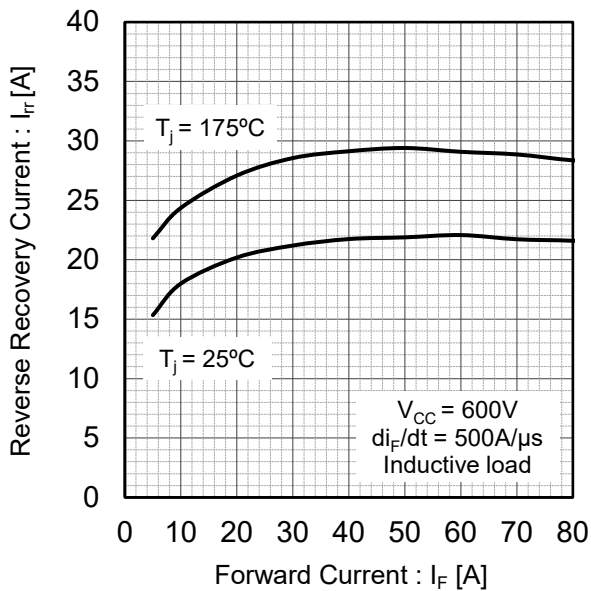
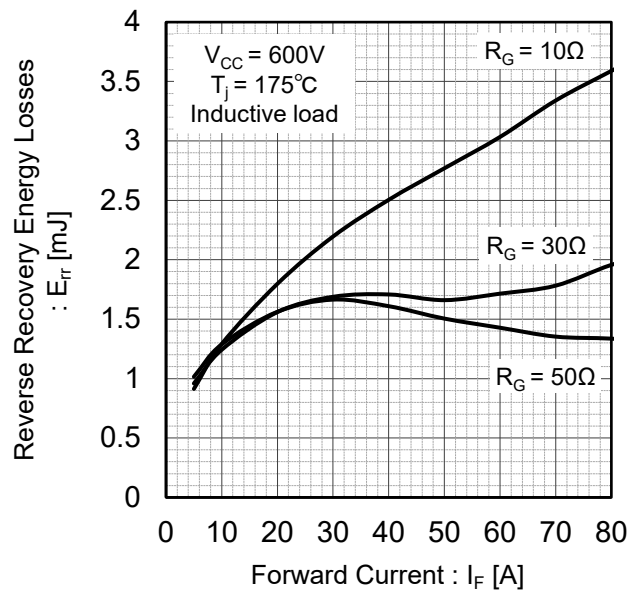


Fig.20 Typical Diode Reverse Recovery Energy Losses vs. Forward Current



●Electrical Characteristic Curves

Fig.21 IGBT Transient Thermal Impedance

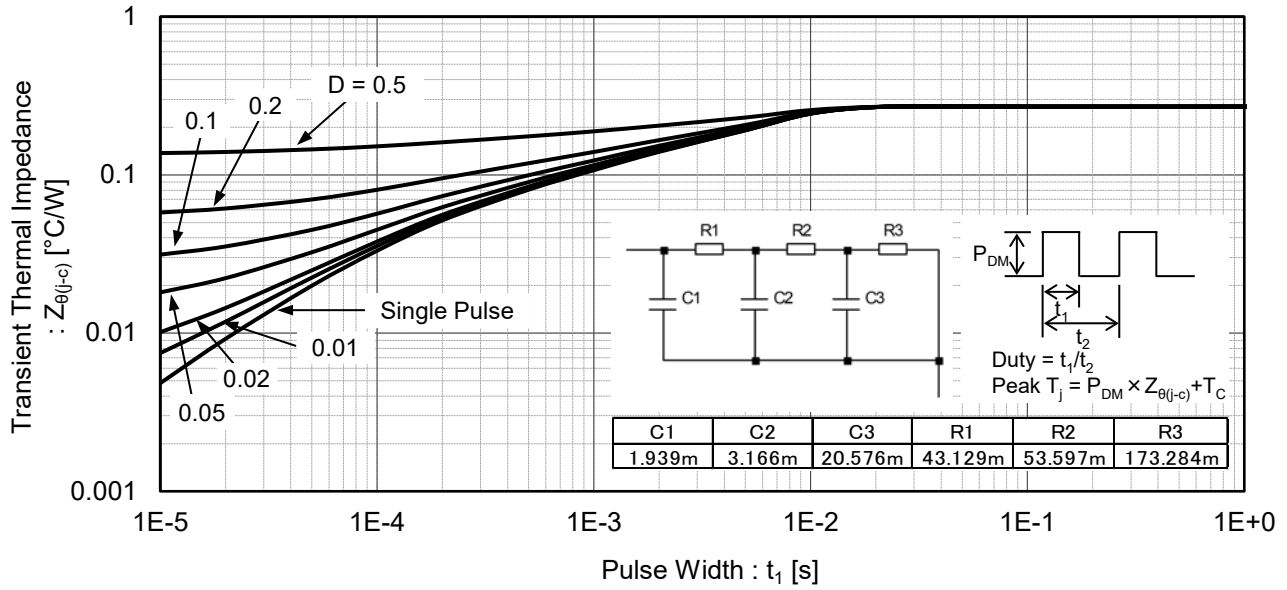
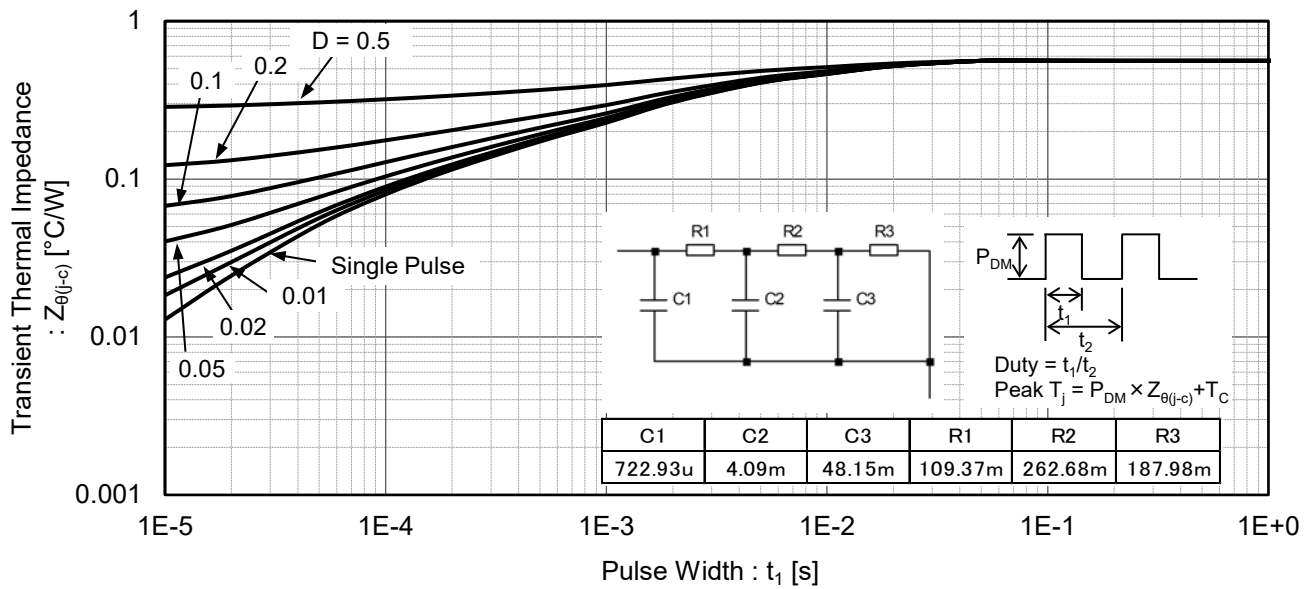


Fig.22 Diode Transient Thermal Impedance



● Inductive Load Switching Circuit and Waveform

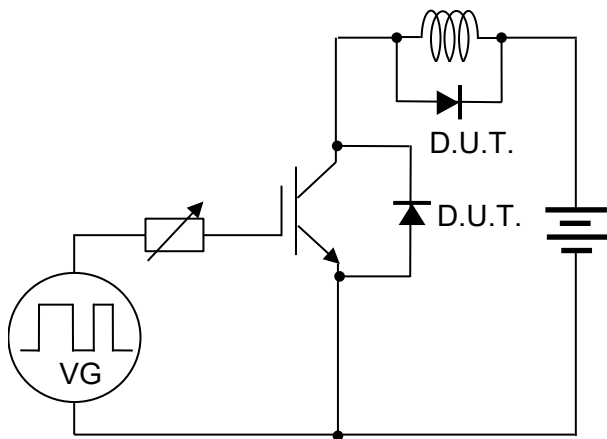


Fig.23 Inductive Load Circuit

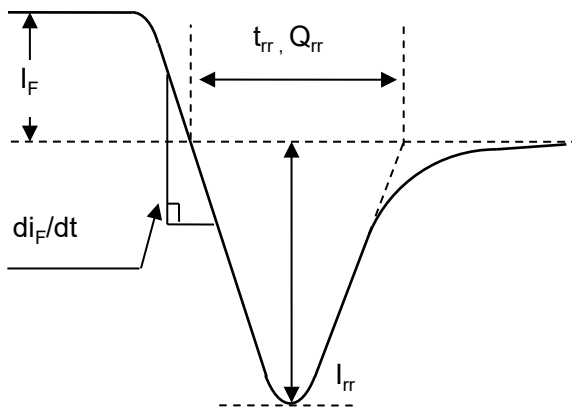


Fig.24 Diode Reverse Recovery Waveform

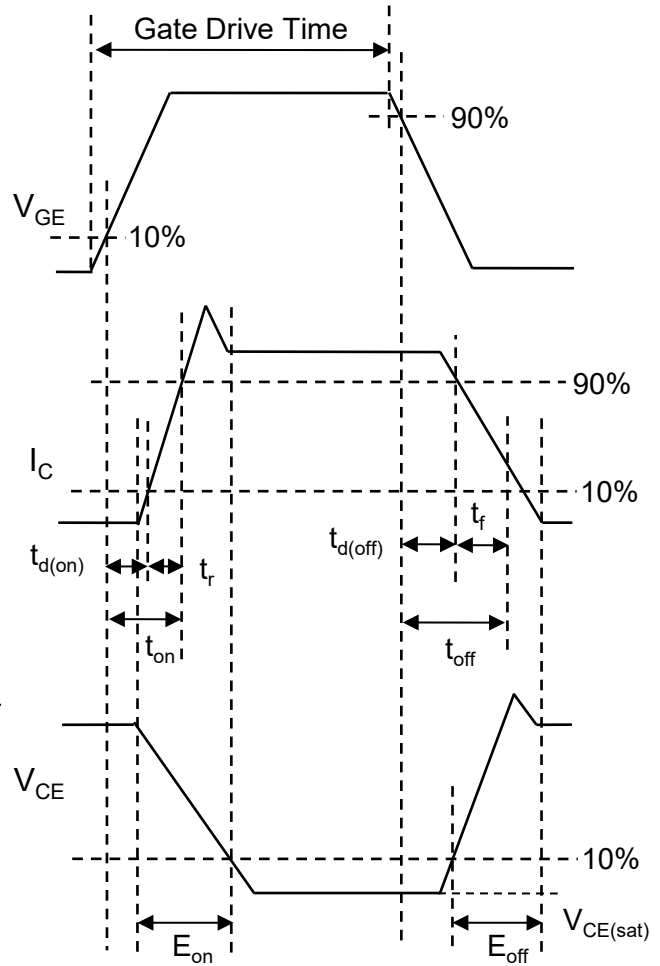


Fig.25 Inductive Load Waveform